

XC-2 XCard Port Map

(VERSION 9.3.0)



2009/03/13

Authors:

XMOS LTD.

Copyright © 2009, XMOS Ltd.
All Rights Reserved

PACKAGE	PORTS					XC-2				
	Ball name	1b	4b	8b	16b	32b	XCore0	XCore1	XCore2	XCore3
XnD0	P1A0						SPI_MISO	X1PORTA HEADER	X2PORTA HEADER	X3PORTA HEADER
XnD1	P1B0						SPI_SS			
XnD2		P4A0	P8A0	P16A0	P32A20					
XnD3		P4A1	P8A1	P16A1	P32A21					
XnD4		P4B0	P8A2	P16A2	P32A22	XL_UP1				
XnD5		P4B1	P8A3	P16A3	P32A23	XL_UP0				
XnD6		P4B2	P8A4	P16A4	P32A24	XL_DN0				
XnD7		P4B3	P8A5	P16A5	P32A25	XL_DN1				
XnD8		P4A2	P8A6	P16A6	P32A26					
XnD9		P4A3	P8A7	P16A7	P32A27					
XnD10	P1C0						SPI_CLK	X1PORTB HEADER	X2PORTB HEADER	X3PORTB HEADER
XnD11	P1D0						SPI_MOSI			
XnD12	P1E0						KEYLEDA			
XnD13	P1F0						KEYLEDB			
XnD14		P4C0	P8B0	P16A8	P32A28		KEYA			
XnD15		P4C1	P8B1	P16A9	P32A29					
XnD16		P4D0	P8B2	P16A10			KEYB			
XnD17		P4D1	P8B3	P16A11						
XnD18		P4D2	P8B4	P16A12						
XnD19		P4D3	P8B5	P16A13						
XnD20		P4C2	P8B6	P16A14	P32A30					
XnD21		P4C3	P8B7	P16A15	P32A31					
XnD22	P1G0						UART_DN	X1LEDA X1LEDB	X2LEDA X2LEDB	X3LEDA X3LEDB
XnD23	P1H0						UART_UP			
XnD24	P1I0						X0LEDA			
XnD25	P1J0						X0LEDB			
XnD26		P4E0	P8C0	P16B0		PROTOTYPE AREA				
XnD27		P4E1	P8C1	P16B1						
XnD28		P4F0	P8C2	P16B2						
XnD29		P4F1	P8C3	P16B3						
XnD30		P4F2	P8C4	P16B4						
XnD31		P4F3	P8C5	P16B5						
XnD32		P4E2	P8C6	P16B6						
XnD33		P4E3	P8C7	P16B7						
XnD34	P1K0									
XnD35	P1L0									
XnD36	P1M0		P8D0	P16B8				ETH_RXD[0:1]		
XnD37	P1N0		P8D1	P16B9						
XnD38	P1O0		P8D2	P16B10				ETH_TXD[0:3]		
XnD39	P1P0		P8D3	P16B11						
XnD40			P8D4	P16B12				ETH_RXD[2:3]		
XnD41			P8D5	P16B13				ETH_TXCLK		
XnD42			P8D6	P16B14				ETH_TXEN		
XnD43			P8D7	P16B15				ETH_RXCLK		
NC								ETH_RXDV		
NC								ETH_RXER		
NC								ETH_MDC		
NC										
XnD49						P32A0				
XnD50						P32A1				
XnD51						P32A2				
XnD52						P32A3				
XnD53						P32A4				
XnD54						P32A5				
XnD55						P32A6				
XnD56						P32A7				
XnD57						P32A8				
XnD58						P32A9				
NC										
NC										
XnD61						P32A10				
XnD62						P32A11				
XnD63						P32A12				
XnD64						P32A13				
XnD65						P32A14				
XnD66						P32A15				
XnD67						P32A16				
XnD68						P32A17				
XnD69						P32A18				
XnD70						P32A19				
NC										

1 Document History

Date	Version	Details
2009/03/13	9.3.0	First Release

XMOS Ltd is the owner or licensee of this design, code, or Information (collectively, the "Information") and is providing it to you "AS IS" with no warranty of any kind, express or implied and shall have no liability in relation to its use. XMOS Ltd makes no representation that the Information, or any particular implementation thereof, is or will be free from any claims of infringement and again, shall have no liability in relation to any such claims.

(c) 2009 XMOS Limited - All Rights Reserved